





5Gbps QSGMII to x4 100BaseFX/1000BaseX port conversion and each port can also be set to SGMII_MAC

5Gbps QSGMII to x4 SGMII_PHY port conversion

5Gbps QSGMII to x4 SGMII_MAC port conversion

Supports unidirection in 1000BaseSX/100BaseFX

SerDes Test pattern

PRBS7/10/31

IDLE /K28.5 /D5.6

Customized define by user

SerDes BIST

Packet Generator and Checker

Low power consumption

Easy layout, good EMI/EMS, and good thermal performance

Supports 25MHz/50MHz crystal or 3.3V OSC input

3.3V and 1.2V power supply

SyncE clock output

QFN-88 E-PAD package

